

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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application of: Levine et al.

Serial No.: 10/675,721

Filed: September 30, 2003

For: Method and Apparatus to **Autonomically Select Instructions for** 

**Selective Counting** 

PATENT TRADEMARK OFFICE CUSTOMER NUMBER

Group Art Unit: 2191

Examiner: Nahar, Qamrun

Attorney Docket No.: AUS920030483US1

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- Supplemental Information Disclosure Statement;
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- Reference AC; and
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Respectfully submitted,

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### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Levine et al.

Serial No: 10/675,751

Filed: September 30, 2003

Title: Method and Apparatus to Autonomically Select Instructions for

**Selective Counting** 

Group Art Unit: 2191

Intellectual Property Law Department International Business Machines Corporation 11400 Burnet Road Austin, Texas 78758

# SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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DOCKET NUMBER AUS920030483US1

No item of information cited in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in this Supplemental Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of this Supplemental Information Disclosure Statement.

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Substitute for form 1449/PTO NFORMATION DISCLOSURE

	Complete if Known
Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Nahar Qamrun
Art Unit	2191
Attorney Docket No.	AUS920030483US1

			Attorney Docket No. AU392003046303		<u> </u>			
Examiner Initials	Document Number	Publication MM-DD-Y		e of Patentee or it of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevan Figures Appear			
AA	5,822,578	Oct. 13, 1998	Frank et	al.				
AB	6,574,727 B1	Jun. 3, 2003	Jun. 3, 2003 Davidson et al.					
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Examiner Initial	Foreign Patent Documer	Publication	n Date Nam	ne of Patentee or nt of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
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Examiner nitial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.							
AC	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.							

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